



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Confirmation No.: 3133

Kenji SUZUKI

Art Unit: 3811

Serial No.: 10/058,005

Examiner: J. Im

Filed: January 29, 2002

Docket No.: 100353-00096

For: SEMICONDUCTOR INTEGRATED CIRCUIT CONFIGURED TO SUPPLY
SUFFICIENT INTERNAL CURRENT

RESPONSE

Assistant Commissioner For Patents
Washington, D.C. 20231

April 18, 2003

Sir:

Responsive to the Office Action dated December 19, 2002, the period for response having been extended to April 19, 2003, entry of the following amendment is respectfully requested.

IN THE CLAIMS

Please cancel Claim 2 without prejudice or disclaimer.

Please amend Claims 1 and 10, as follows:

1. (Amended) A semiconductor integrated circuit, comprising:

pads;

a first power supply I/O cell which is connected to an external pin through a corresponding one of said pads;

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